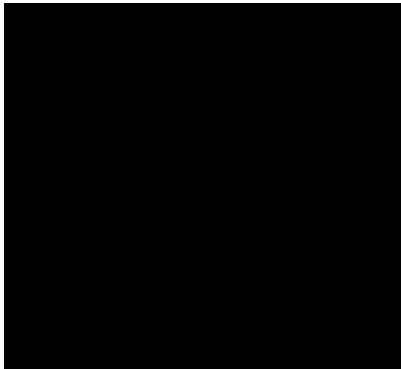
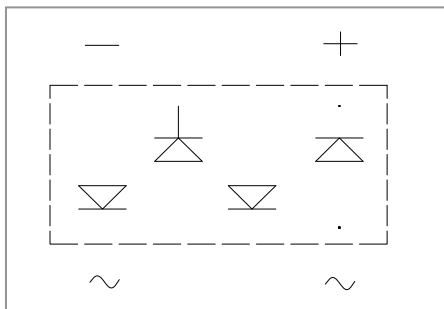




- UL recognition, file #E313149
- Ideal for automated placement
- Glass passivated chip junction
- High surge current capability



- " : Tin plated leads, solderable per J-STD-002 and JESD22-B102
- As marked on body

■ (Ta=25 Unless otherwise specified

	IO	A	1.5				
Forward Surge Current (Non-repetitive) @60Hz Half-sine wave, 1 cycle, Tj=25	IFSM	A	50				
Forward Surge Current (Non-repetitive) @1ms, square wave, 1 cycle, Tj=25			100				
Current squared time @1ms≤t<8.3ms Tj=25 Rating of per diode	I ² t	A ² s	10.4				
Storage temperature	T _{stg}		-55 ~ +150				
Junction temperature	T _j		-55 ~ +150				

■ Ta=25 Unless otherwise specified

Maximum instantaneous forward voltage drop per diode	V _F	V	I _{FM} =0.7A	0.95				
Maximum DC reverse current at rated DC blocking voltage per diode	I _R	μA	T _j =25	5				
			T _j =125	100				
Typical junction capacitance	C _j	pF	Measured at 1MHz and Applied Reverse Voltage of 4.0 V.D.C	17				



■ Ta=25 Unless otherwise specified

Thermal Resistance	Between junction and ambient	RθJ-A	/W	62.5			
	Between junction and lead	RθJ-L		25.0			
	Between junction and case	RθJ-C		8.0			

Note: Device mounted on P.C.B with 35mm*25mm*1.7mm

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Dim	Min	Max
A	4.30	4.50
B	6.00	6.40
C	3.90	4.10
D	4.90	5.10
E	1.25	1.45
F	1.60 Max	
G	0.60	0.70
H	0.15	0.25
I	0.30	0.80



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